

Action Item List 21-Oct-03

Nb.	What	Who	When	Status
General Statements				
1.1	We should keep TVT schedule (J+JT+JPD test this year)			Done
1.2	CSIST will do their best in production and testing			Done
1.3	Do not coat Rad Hard PROM area			
1.4	For FPGA replacement try on JBU board first			FPGA replacement was tried and succeeded.
1.5	Put Rad Hard PROMs in JMDC-1 -- most like-flight, put JIM-AMSW&1553 with STS channel there (TBC with NSPO)			Done
1.6	MIT/CERN will produce board ACOP6H not T			A.Rozhkov will give schematics to C.Wan for checking, then production.
1.7	MIT/CERN will produce board ACOP6H outside CERN			
1.8	JINJ will be TVT tested separately in Apr. 2004			
1.9	Let us not change 1553 ISS address during TVT, this simplifies JLIF control box			Done
1.10	Use only one AMS-Wire out for each JINJ, not two			Done
1.11	EMI test for J+JPD only, without JT (JINJ not ready)			
1.12	CSIST will: produce front panels, replace 2 chips, make conformal coating for 80 flight quality USCMs, A.Rozhkov will provide assembly notes and perform USCM testing.			Assembly notes and coating drawings ready. Front panel drawings ready, needs more information on connectors holes. We need all FM USCM at CERN to study high temperature problems.
1.13	Board afterproduction test sequence: functional test, conformal coating, ESS test, functional test			Done
JSBC				
2.1	Finalize FPGA firmware functions	P.Hong, A.Kounine		Done
2.2	Program FPGA with new firmware	H.Chang	15-Aug	Done
2.3	Verify and test FPGA firmware	P.Hong, A.Kounine	15-Aug	Done
2.4	Replace Actel FPGA chip on two JSBC boards	M.Lin		Done (for 4 boards)
2.5	New ROM monitor to W.P.Yang	P.Hong	20-Aug	Done
2.6	Program 1 RadHard PROM with new ROM monitor	H.Chang	20-Aug	Done
2.7	Assemble RadHard PROM and remove flash memory from this board (NO COATING)	M.Lin	20-Aug	Done
2.8	Remove diode and resistor added for old backplane on all JSBC boards	H.Chang	25-Aug	Done
2.9	Heat sink problem	A. Wu, Y. Ting	30-Aug	A.Wu: JPD thermal model predicts correct temperatures with wrong heat dissipation input. A.Kounine: JSBC heat sinks should be separated. A.Wu: will prepare preliminary report TBD during TIM.

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JIM-CAN				
3.1	Program PCI agent FPGA with new PCI core	J.Liu		Done
3.2	Verify and test new PCI firmware	S.Xu, J.Liu		Done
3.3	Replace PCI agent FPGA on all JIM-CAN boards	M.Lin		Done
3.4	Finalize CAN control FPGA functions	S.Xu, F.Yeh		Done
3.5	Program new CAN control FPGA	F.Yeh		Done
3.6	Verify and test new CAN control firmware	S.Xu, C.Chao		Done
3.7	Replace CAN control FPGA on all JIM-CAN boards	M.Lin		Done
3.8	Finalize 8051 software	S.Xu		Done
3.9	Program 1 RadHard PROM with new 8051 code	S.Xu	20-Aug	Done
3.10	Assemble RadHard PROM to one JIM-CAN	M.Lin	20-Aug	Done
JBU				
4.1	Assemble two new JBU board with new layout	R.Guo		Done
4.2	Program new FPGA with new PCI core, DMA function and parity check	R.Guo		Done
4.3	Verify and test new JBU boards	P.Hong, X.Cai	22-Aug	Done
JIM-AMSW/1553				
5.1	Program PCI Agent FPGA with new PCI core	C.Lin		Done
5.2	Verify and test new PCI agent	X.Cai		Done
5.3	Replace PCI agent FPGA on two JIM-AMSW/1553 boards	M.Lin		Done
5.4	Program new AMSWire FPGA	C.Lin		Done
5.5	Verify and test new AMSWire FPGA	M.Lin		Done
5.6	Replace new AMSWire FPGA on two JIM-AMSW/1553 boards	M.Lin		Done
5.7	Add magnetic shielding for 1553 transformer (check if enough material)	M.Lin, V.Koutsenko	22-Aug	Done
JLIF				
6.1	Add magnetic shielding for 1553 transformer (check if enough material)	M.Lin, V.Koutsenko	22-Aug	Done
JIM-HRDL/422				
7.1	Program PCI Agent FPGA with new PCI core	J.Liu		Done
7.2	Verify and test new PCI Agent	X.Cai, J.Liu		Done
7.3	Replace PCI Agent FPGA on two JIM-HRDL/422 boards	M.Lin		Done
7.4	Finalize HRDL/422 FPGA functions	C.Wan, P.Dennett , V.Koutsenko, A.Kounine, A.Lebedev, X.Cai	8-Aug	C.Wan prepares new firmware document, we should go ahead with current decision. C.Wan will distribute her document via e-mail.
7.5	Program HRDL/422 FPGA new firmware	C.Wan	2 months after #7.4	Started programming
7.6	Study extinction ratio with the help of AS	A.Rozhkov, V.Koutsenko	this week	Done

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Nb.	What	Who	When	Status
JBP				
8.1	Repair old new JBP			Done
8.2	Produce new new JBP PCB	M.Lin	15-Aug	- one JBP in PCB production, ready in 2 weeks, we need only PCB, not PCA. - one JBP under assembly, Y.Ting will bring to CERN this Saturday. - one JBP assembled with J-Crate; - should we replace cPCI connectors from solder to press type? TBD during TIM.
8.3	Produce new new JBP PCA if old new JBP is still bad	M.Lin	18-Aug	
8.4	Test old new JBP in the J-Crate	X.Cai	15-Aug	
8.5	Test new new JBP in the J-Crate if needed	X.Cai		
Mechanics & Thermal				
9.1	Cable slot	B.Pan	20-Aug	
9.2	Fix problem of holes to fix front panel to crate	B.Pan		Done
9.3	Long side wall bending ??	X.Cai, B.Pan		Done
9.4	Drawings for cable support to CSIST	M.Capell	1-Oct	
9.5	Thermostats and heaters design	M.Capell	1-Sep	
JPD				
10.1	JPD test setup readines for boards	A.Koulemzine	18-Aug	Done
10.2	JPD test setup readines: for crate	A.Koulemzine	1-Sep	Done
10.3	Ship last missing JPD part to CSIST	N.Wood	6-Aug	Done , parts received.
10.4	Detailed JPD QM design specifications	A.Koulemzine	1-Nov	Done
10.5	EMC and ESS facilities drawings (floor plan) and visit	A.Koulemzine C.Lin	11-Aug	Done
10.6	Test JPD program working via X-windows remotely (this saves 60m CAN cable)	CXLHK		Done
10.7	Connectors for JPD cables		22-Aug	Done
JINJ				
11.1	QM design of JINJ to CSIST	V.Koutsenko	26-Aug	Done
JINF				
12.1	QM2 design of JINF to CSIST	V.Koutsenko		Done
12.2	Actel chips for JINF to CSIST	V.Koutsenko	1-Sep	W.P.Yang programmed all ACTELs for JINF and JINJ at CSIST

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Nb.	What	Who	When	Status
TVT Preparation				
13.1	Test STS+ISS 1553 buses combined (this saves 60m 1553cable)	CXLHK		Done
13.2	Check optical feedthroughs (bring them from NSPO to CSIST)	X.Cai		Done
13.3	Test all electrical feedthroughs connection before going to NSPO	X.Cai		Done
13.4	Search for 21-pin connector for JLIF control	X.Cai		Done
13.5	Modify JLIF control box & combine with JHIF control box and cables	X.Cai, Y.Ting		Done
13.6	Build 2 Dallas bus cables for J and JT crate	A.Koulemzine, C.Lin	18-Aug	Done
13.7	Deliver vacuum wires and connectors for AMS-Wire cables for TVT	N.Wood	31-Aug	Done
13.8	Build 70 vacuum AMS-Wire cables for TVT	Y.Ting	5 days after #13.7	Done
13.9	Functional procedure description for TVT to NSPO	X.Cai	25-Aug	Done