

Action Item List 07-Oct-04

Nb.	What	Who	When	Status
J-Crate FM+FS Production				
1.1	- CSIST keeps original schedule for J+JPD+JT FM+FS production - solder flash memories for tests, do not coat memory areas - do not deliver to CERN till Aug-Sep 2005 - when ROM monitor software is ready - solder RHRoMs, coat and test	Y.Ting		J FM/FS module production: Jul 15-Dec 31 JT FM/FS module prod.: Jul 15-Dec 31 JPD QM2 testing: Sep 1-Oct 15 JPD FM/FS module prod.: Oct 12-Dec 31 J+JT+JPD Crate mech: Jul 15-Dec 31 J+JT+JPD Crate ESS/EMI: Jan 1- Feb 15 J+JT+JPD Crate TVT: Mar 15-Mar 31 J+JT+JPD Crate delivery: Apr 1-Apr 15 <i>(see below individual boards status)</i>
1.2	Decided not to change Actel PCI core firmware			
1.3	Full qualification tests for J-Crate FM+FS in 2004/2005			
1.4	Board level test software for these tests	C.H.Lin	1-Oct	List of test modifications is ready to be reviewed by XC and AK. Finalize @TEM.
JSBC				
2.1	Thermal (?) test of JSBC QM2 V.2 at CERN	X.Cai	1-Oct	JSBC Flash memory problems, will bring to Houston.
2.2	New heat sink drawing for review by VK and AK	W.P.Yang		Modify drawing - RISC-Watch header problems.
2.3	Manufacture new JSBC heat sink	W.P.Yang		
2.4	Send new heat sink to CERN	Y.Ting		
2.5	Thermal test of new JSBC	X.Cai	+2wks	
JBU				
3.1	Modify firmware (DMA)	W.P.Yang	1-Oct	Under tests by AK, results next week.
JIM-CAN				
4.1	Design review	W.P.Yang		PCB in production. Decide @TEM which PROM to put (radhard or flash). HBB and SX will test JIM-CAN with USCMS as much as possible (including "bypass mode").

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JIM-AMSW/1553				
5.1	Modify PCB layout (Impedance problems)	W.P.Yang	15-Oct	Line width changed with changing the PCI connector (to press-fit).
JIM-HRDL/422				
6.1	Check frame number problem (RS422)	C.Wan		XC will make more tests, check with PD.
6.2	Layout check (Impedance problems)	W.P.Yang	15-Oct	
JHIF				
8.1	HRDL testing	V.Koutsenko A.Kounine	15-Oct	Testing completed. TX output on margin, but VK is happy. Writing the document. Will present @TEM.
8.2	Bad receiver, ask the producer	M.Capell	1-Nov	No response from producer
8.3	Get split PVDF sleeve sample and ZIP tool for installations outside of FIT.	M.Capell	1-Oct	ZIP tool arrived, no sleeve yet, should decide what to do (LM, help!).
8.4	New front panel for JHIF	W.P.Yang	1-Dec	In production. Will pickup front panels @TEM.
8.5	New way of fibers mounting: VK is worried.			
JBP				
9.1	JBP v4 tests	A.Kounine V.Koutsenko	1-Oct	Testing. "GO" for production from VK on 20-Nov-2004.
Mechanics & Thermal				
10.1	More new card-locks to buy.	M.Capell		There are 204 cardlocks at CSIST, and 243 at CERN. We have to buy 700 cardlocks more. Distribution Table is available.
10.6	Study Dallas sensor connection for S-Crate	A.Koulemzine	1-May	Input from Diego received should be discussed with MC and AL.
10.7	Dallas sensors for TRD (W.Karpinsky - contact person)	A.Koulemzine		some info next week
10.8	Dallas sensors for ECAL (F.Spinella & F.Cadoux - contact persons)	A.Koulemzine		some info next week
10.9	512 Dallas sensors for the test setup	A.Koulemzine		Dallas sensors late delivery (MC and NW will take care).

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JPD				
11.4	Problems with FM parts	A.Koulemzine		work with NW
11.5	"Go" for FM PCA production	A.Koulemzine		more tests needed (AK+AL) - Done. "Go" will be given @TEM.
USCM				
12.1	CSIST will: produce front panels, replace 2 chips, make conformal coating for 40 (total) flight quality USCMs, A.Rozhkov will provide assembly notes and perform USCM testing. (Exact quantity still under revision).	A.Rozhkov		Assembly notes and coating drawings ready. Front panel drawings not ready. MC+AL make a list of all flight quality USCMs needed (coordinate with people at Houston), then AR will send to CSIST final FP drawings with N/B ID to be engraved, etc.
12.3	USCM software	H-B.Broeker	1-Dec	Flight OTP is needed a.s.a.p.
ACOP6T				
13.1	ACOP6T production at CSIST	Y.T.Ting		6 PCA tested OK. Leave these boards at CSIST.
13.2	Find the part number for new optical fiber cables	M.Capell	1-Oct	Part number found, too expensive, try again.
UBPV2				
17.2	Production at CSIST, ship to CERN for testing when ready	Y.Ting	13-Aug	Send 1 to CERN, proceed with PCA
17.3	Testing at CERN	V.Koutsenko		
BOMs to CSIST Format				
18.2	ALL BOMs in CSIST format	Y.Lin	8-Jul	S9053* BOMs missing.
Level-1 Trigger				
19.1	Firmware for SPT2 boards	C.H.Lin	1-Nov	TOF coordination meeting at CERN 27-Oct-2004

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FIT Test in 15-25 Jan 2005 at KSC				
20.1	New ROM monitor and JMDC software	A.Kounine X.Cai	1-Jul	
20.2	Test setup at CERN	X.Cai	1-Aug	
20.3	Testing at CERN	X.Cai	1-Aug	
20.4	Ground software for FIT	A.Lebedev	1-Oct	
20.5	Regular SIM (Software Interchange Meetings)	P.Dennett		
Flight Cables for AMS-Wire and Trigger Signals				
21.2	Order the cables	M.Capell		
Actel Programming and Labelling				
22.1	all T,U JINx Actels to be programmed at CERN	board responsibles		station available, contact Koutsenko for instructions.
22.2	Develop procedure for programming and labelling	V.Koutsenko	1-Sep	5 pgs
TVT Test of J+JT+JPD and T+TPD & U+UPD				
23.1	Material List for TVT test (FM/FS:J+JT+JPD) to NSPO	X.Cai	1-Feb	
23.2	Material List for TVT test (QM2: T+TPD & U+UPD) to NSPO	G.Ambrosi		
23.3	Check flight thermal fillings are used in TVT	M.Molina		
23.4	Discuss TVT interfaces for T+TPD & U+UPD			TEM @ CSIST in November
AMS Integration				
24.1	AMS Electronics integration	A.Lebedev		waiting for advices